

10981866-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#14/Bn
17411
JM

In re Application of:		
Gupta et al.	Examiner:	Makhdoom, S.
Serial No.: 09/378,596)	Art Unit:	2123
Filing Date: August 20, 1999)		RECEIVED
For: AUTOMATIC DESIGN OF)		JUL 2 2 2003
PROCESSOR DATAPATHS)		Technology Center 2100

PRELIMINARY AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION

Hon. Assistant Commissioner for Patents Alexandria VA 22313-1450

Dear Sir:

In response to the Office Action mailed February 10, 2002, Applicants respectfully request reconsideration of the above referenced patent application. Please amend the above identified application as follows and consider the following remarks for allowance of the above identified patent application.

IN THE CLAIMS

CLEAN (UNMARKED) COPY OF AMENDED CLAIMS

Please amend Claim 16 as follows:

16. (Amended) A method for automatic synthesis of a register file and functional unit-

10981866-1

Examiner: Makhdoom, S.

Serial No.: 09/378,596

Group Art Unit: 2123